

FIG. 1

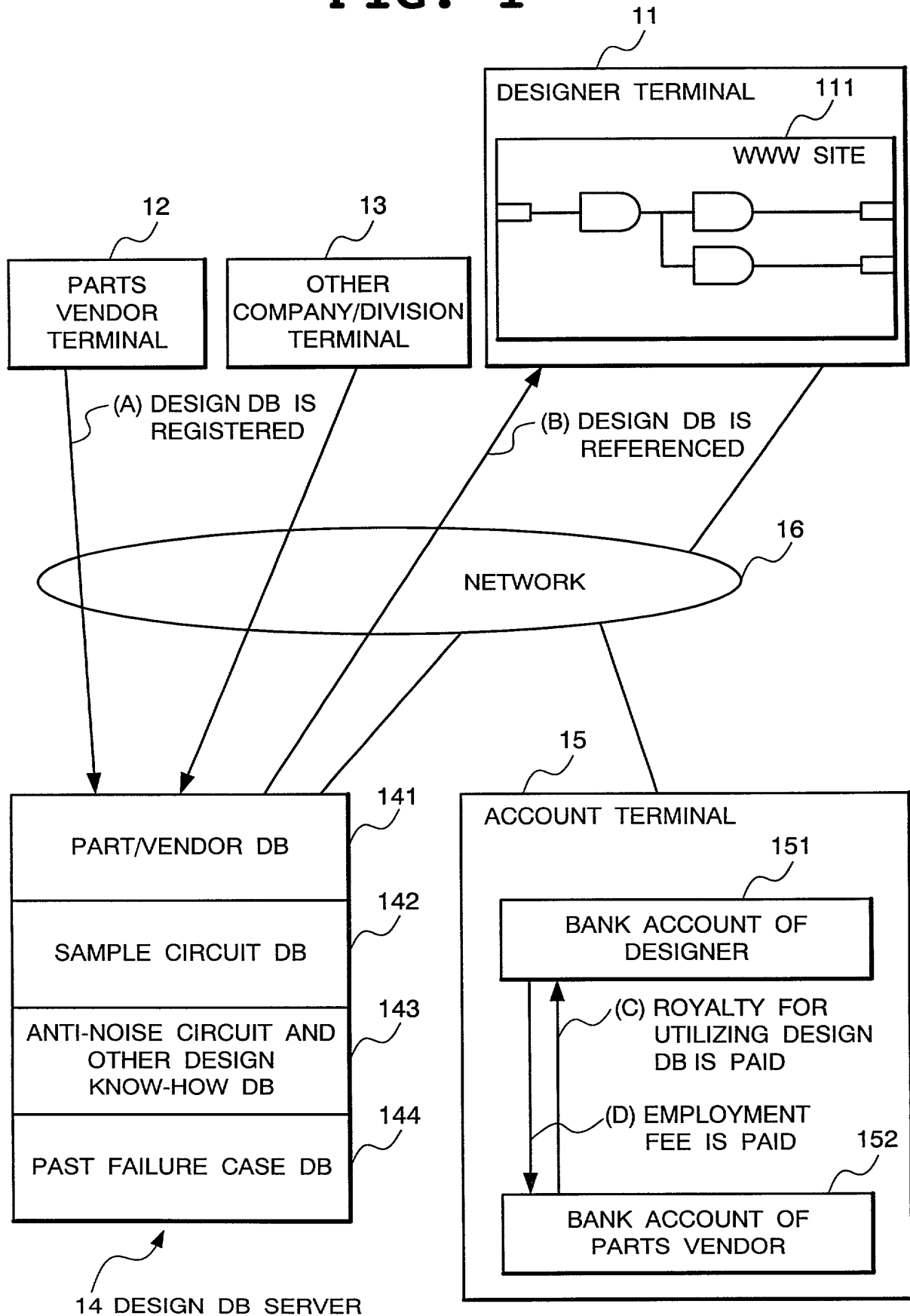


FIG. 2

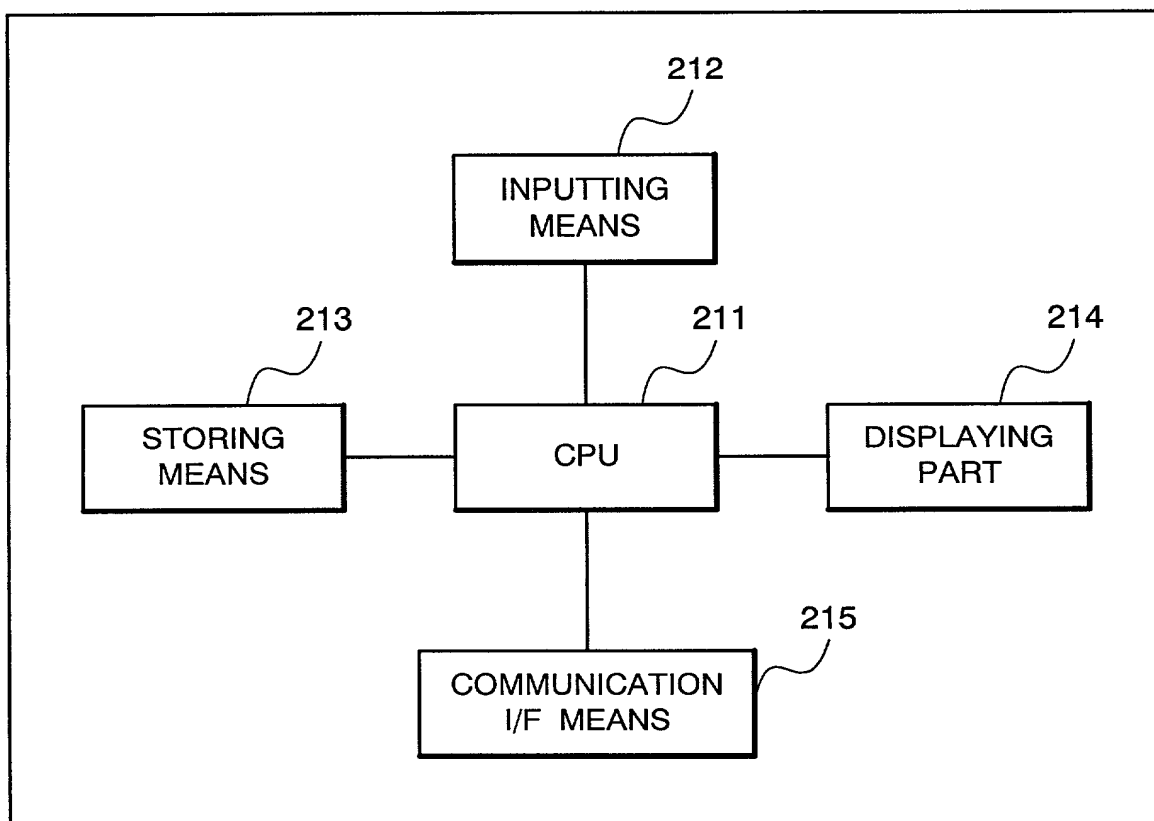


FIG. 2

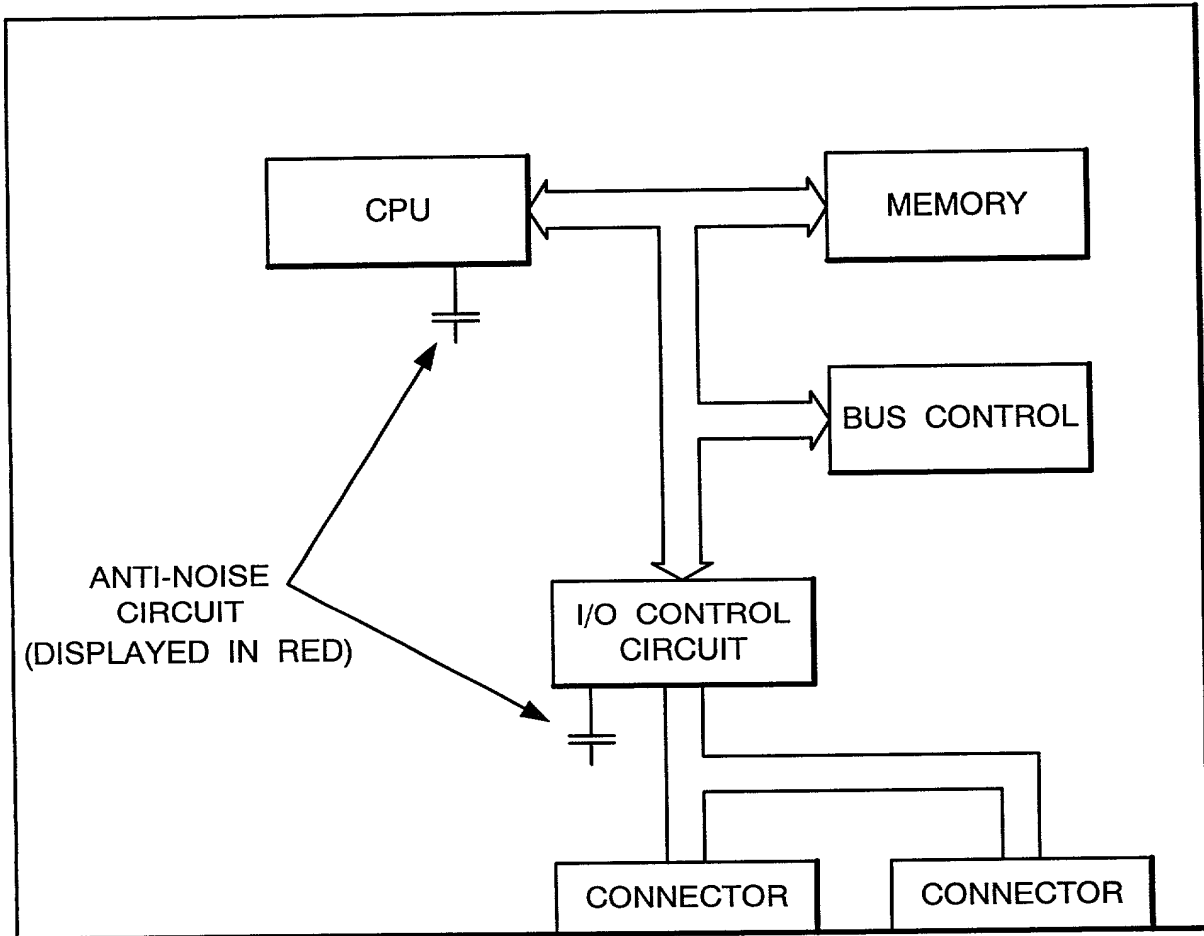
FIG. 3

PART/VENDOR DB, etc.	PART	VENDOR	PRICE	SPECIFICATION
	CPU	CPU XX	CPU YY	CPU ZZ
	MEMORY	MEMORY XX	MEMORY YY	MEMORY ZZ

SAMPLE CIRCUIT DB (AROUND CPU , AROUND MEMORY , AROUND I/O , etc.)				
ANTI-NOISE CIRCUIT AND OTHER DESIGN KNOW-HOW DB (MEASURES AGAINST STATIC NOISE , REFLECTIVE NOISE , CROSS TALKS NOISE , etc.)				
PAST FAILURE CASE DB				

FIG. 3

FIG. 4



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FIG. 5

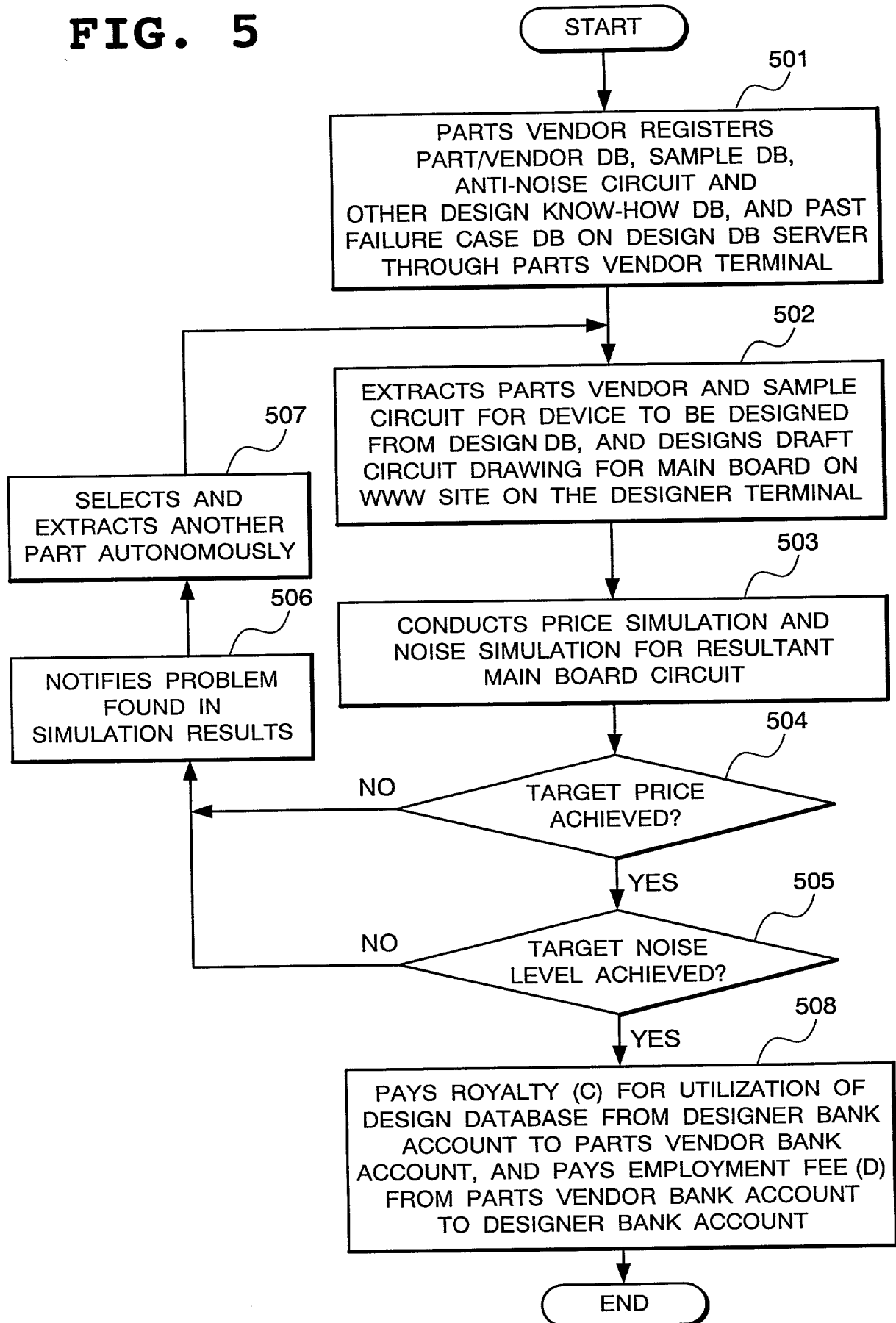


FIG. 5